# **WEST Search History**



DATE: Saturday, October 16, 2004

Hide?	<u>Set</u> <u>Name</u>	Query	<u>Hit</u> Count
	DB=U	SPT; PLUR=YES; OP=ADJ	
	L15	11 and L14	25
	L14	L13 and client and server and (metadata or meta data)	38
	L13	L12 and 709/2\$\$.ccls.	152
	L12	(facilitat\$ same address\$2) and (email or e-mail or electronic mail) and (url or uniform resource locator) and (web adj2 page\$)	324
	L11	facilitat\$.ti. and address.ti. and (email or e-mail or electronic mail) and (url or uniform resource locator) and (web adj2 page\$)	0
	L10	facilitat\$.ti. and address.ti. and (email or e-mail or electronic mail)	2
	L9	facilitat\$.ti. and address.ti.	16
	L8	facilitat\$2.ti. and address.ti. and email.ti.	0
	L7	indicat\$4 same ((email or e-mail or electronic mail) adj3 message\$) and L6	4
	L6	(url or uniform resource locator) and (web adj2 page\$) and L3 and 709/2 \$\$.ccls. and client and server and (metadata or meta data)	33
	L5	(url or uniform resource locator) and (web adj2 page\$) and L3 and 709/2 \$\$.ccls.	130
	L4	(url or uniform resource locator) and (web adj2 page\$) and L3	267
	L3	(facilita\$ same access) and L1	958
	L2	(faciliat\$ same access) and L1	0
	L1 .	(manag\$ or control\$4) same (email or e-mail or electronic mail)	6365

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# **Hit List**



## Search Results - Record(s) 1 through 4 of 4 returned.

☑ 1. Document ID: US 6714979 B1

L7: Entry 1 of 4

File: USPT

Mar 30, 2004

US-PAT-NO: 6714979

DOCUMENT-IDENTIFIER: US 6714979 B1

TITLE: Data warehousing infrastructure for web based reporting tool



### ☑ 2. Document ID: US 6631402 B1

L7: Entry 2 of 4

File: USPT

Oct 7, 2003

US-PAT-NO: 6631402

DOCUMENT-IDENTIFIER: US 6631402 B1

TITLE: Integrated proxy interface for web based report requester tool set



#### ☑ 3. Document ID: US 6615258 B1

L7: Entry 3 of 4

File: USPT

Sep 2, 2003

US-PAT-NO: 6615258

DOCUMENT-IDENTIFIER: US 6615258 B1

TITLE: Integrated customer interface for web based data management



#### ☑ 4. Document ID: US 6377993 B1

L7: Entry 4 of 4

File: USPT

Apr 23, 2002

US-PAT-NO: 6377993

DOCUMENT-IDENTIFIER: US 6377993 B1

TITLE: Integrated proxy interface for web based data management reports

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Full Title Citation Front Review

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# **Display Format:**

Previous Page

Next Page

Go to Doc#

# **Hit List**



### Search Results - Record(s) 1 through 16 of 16 returned.

☐ 1. Document ID: US 6654869 B1

L9: Entry 1 of 16

File: USPT

Nov 25, 2003

US-PAT-NO: 6654869

DOCUMENT-IDENTIFIER: US 6654869 B1

\*\* See image for Certificate of Correction \*\*

TITLE: Assigning a group tag to an instruction group wherein the group tag is recorded in the completion table along with a single instruction <u>address</u> for the group to <u>facilitate</u> in exception handling

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw De

☐ 2. Document ID: US 6654793 B1

L9: Entry 2 of 16

File: USPT

Nov 25, 2003

US-PAT-NO: 6654793

DOCUMENT-IDENTIFIER: US 6654793 B1

TITLE: System and method for  $\underline{\text{facilitating}}$  dynamic loading of stub information to enable a program operating in one  $\underline{\text{address}}$  space to invoke processing of a remote method or procedure in another address space

Full	Title	Citation	Front	Review	Classification	Date	Reference	35005000	attachments	Claims	KWMC	Drawi De
								19 119 1119				

☐ 3. Document ID: US 6557037 B1

L9: Entry 3 of 16

File: USPT

Apr 29, 2003

US-PAT-NO: 6557037

DOCUMENT-IDENTIFIER: US 6557037 B1

\*\* See image for Certificate of Correction \*\*

TITLE: SYSTEM AND METHOD FOR EASING COMMUNICATIONS BETWEEN DEVICES CONNECTED RESPECTIVELY TO PUBLIC NETWORKS SUCH AS THE INTERNET AND TO PRIVATE NETWORKS BY FACILITATING RESOLUTION OF HUMAN-READABLE ADDRESSES



☐ 4. Document ID: US 6553222 B1

L9: Entry 4 of 16

File: USPT

Apr 22, 2003

US-PAT-NO: 6553222

DOCUMENT-IDENTIFIER: US 6553222 B1

TITLE: Method and system <u>facilitating</u> automatic <u>address</u> book entries with caller

specific voice identifiers and call notification

Full Title Citation Front Review Classification Date Reference **Sequences Altacliments.** Claims KMC Draw De

☐ 5. Document ID: US 6473431 B1

L9: Entry 5 of 16

File: USPT

Oct 29, 2002

US-PAT-NO: 6473431

DOCUMENT-IDENTIFIER: US 6473431 B1

\*\* See image for Certificate of Correction \*\*

TITLE: System and method <u>facilitating</u> determination by router nodes in a network of ranges of addresses for which each router node is responsible

Full Title Citation Front Review Classification Date Reference **Sequences Altectments:** Claims KMC Draw De

☐ 6. Document ID: US 6377161 B1

L9: Entry 6 of 16

File: USPT

Apr 23, 2002

US-PAT-NO: 6377161

DOCUMENT-IDENTIFIER: US 6377161 B1

TITLE: Method and apparatus in a wireless messaging system for <u>facilitating</u> an

exchange of address information

Full Title Citation Front Review Classification Date Reference Council Attachment Claims WMC Draw De T. Document ID: US 6192424 B1

L9: Entry 7 of 16 File: USPT Feb 20, 2001

US-PAT-NO: 6192424

DOCUMENT-IDENTIFIER: US 6192424 B1

TITLE: Bus arbiter for <u>facilitating</u> access to a storage medium in enhanced burst

е

mode using freely specifiable address increments/decrements

Full Title Citation Front Review Classification Date Reference **Sequences Altachments** Claims KWC Draw De

h eb b g ee e f

ef b

е

☐ 8. Document ID: US 6076176 A

L9: Entry 8 of 16

File: USPT

Jun 13, 2000

US-PAT-NO: 6076176

DOCUMENT-IDENTIFIER: US 6076176 A

TITLE: Encoding of failing bit addresses to facilitate multi-bit failure detect

using a wired-OR scheme

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWC Draw December 19. Document ID: US 5991866 A

L9: Entry 9 of 16 File: USPT Nov 23, 1999

US-PAT-NO: 5991866

DOCUMENT-IDENTIFIER: US 5991866 A

TITLE: Method and system for generating a program to <a href="facilitate">facilitate</a> rearrangement of

 $\underline{\mathtt{address}}$  bits among  $\underline{\mathtt{addresses}}$  in a massively parallel processor system

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

10. Document ID: US 5933858 A

L9: Entry 10 of 16 | File: USPT | Aug 3, 1999

US-PAT-NO: 5933858

DOCUMENT-IDENTIFIER: US 5933858 A

TITLE: Efficient internal <u>address</u> encoding scheme for an integrated circuit which

<u>facilitates</u> multiple addressing modes

Full Title Citation Front Review Classification Date Reference Section Sex Attachineria Claims ROMC Draws De 

11. Document ID: US 5911153 A

L9: Entry 11 of 16 File: USPT Jun 8, 1999

US-PAT-NO: 5911153

DOCUMENT-IDENTIFIER: US 5911153 A

TITLE: Memory design which <u>facilitates</u> incremental fetch and store requests off

applied base <u>address</u> requests

\_\_Full.\_\_| Title\_| Citation | Front | Review | Classification | Date | Reference | **Sequences | Alfactiments:** | Clarims | RMC | Drawi-Do

☐ 12. Document ID: US 5572713 A

h e b b g e e e f e e f b e

L9: Entry 12 of 16

File: USPT

Nov 5, 1996

Dec 26, 1995

US-PAT-NO: 5572713

DOCUMENT-IDENTIFIER: US 5572713 A

TITLE: System and method for obtaining correct byte <u>addresses</u> by using logical operations on 2 least significant bits of byte <u>address to facilitate</u> compatibility between computer architectures having different memory orders

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

13. Document ID: US 5564057 A

L9: Entry 13 of 16 File: USPT Oct 8, 1996

US-PAT-NO: 5564057

DOCUMENT-IDENTIFIER: US 5564057 A

TITLE: Microprocessor architecture which <u>facilitates</u> input/output utilizing pairs of registers which the same address

Full Title Citation Front Review Classification Date Reference Sequences Attechnicis Claims KMC Draw De

File: USPT

US-PAT-NO: 5479632

DOCUMENT-IDENTIFIER: US 5479632 A

L9: Entry 14 of 16

TITLE: Microcomputer having two-level memory to  $\underline{facilitate}$  calculation of effective addresses

Full Title Citation Front Review Classification Date Reference Section Affection of Claims KMC Draw De 

15. Document ID: US 5398328 A

L9: Entry 15 of 16 File: USPT Mar 14, 1995

US-PAT-NO: 5398328

DOCUMENT-IDENTIFIER: US 5398328 A

\*\* See image for <u>Certificate of Correction</u> \*\*

TITLE: System for obtaining correct byte  $\underline{addresses}$  by XOR-ING 2 LSB bits of byte  $\underline{address}$  with binary 3 to  $\underline{facilitate}$  compatibility between computer architecture having different memory orders

Full | Title | Citation | Front | Review | Classification | Date | Reference | Seguences | Altachments | Claims | KWIC | Draw, Do

ef

b

☐ 16. Document ID: US 4989182 A

L9: Entry 16 of 16

File: USPT

Jan 29, 1991

US-PAT-NO: 4989182

DOCUMENT-IDENTIFIER: US 4989182 A

TITLE: Dynamic random access memory having dummy word line for facilitating reset

of row <u>address</u> latch

Title Citation Front Review Classification Date Reference Sequence	s Altachments Claims KW
Generate Collection Print Fwd Refs & Bkw	vd Refs Generate
Term	Documents
ADDRESS	259261
ADDRESSES	151474
FACILITAT\$	. 0
FACILITAT .	14
FACILITATA	1
FACILITATABLE	11
FACILITATAD	1
FACILITATAE	2
FACILITATAED	1
FACILITATATE	2
FACILITATATED	. 1
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Display Format: TI Change Format

<u>Previous Page</u> <u>Next Page</u> <u>Go to Doc#</u>

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